

## Achronix to Demonstrate Speedster FPGA's 10.3 Gbps SerDes at Inaugural FPGA Summit 2008

San Jose, Calif. —Achronix Semiconductor will demonstrate its 10.3 Gbps embedded serial-deserializer (SerDes), a key element of its 1.5 GHz Speedster FPGAs. The demonstration will be at the inaugural FPGA Summit, Tuesday thru Thursday Dec. 9-11, at the Wyndham Hotel in San Jose, Calif. A video of the SerDes demonstration will be posted to [www.achronix.com](http://www.achronix.com) after Dec. 11.

Enabling applications such as 100 Gigabit Ethernet, the SerDes is an important element of the first member of the Achronix Speedster™ family, the SPD60, which delivers three times the performance of conventional FPGAs using patented picoPIPE technology. The SPD60 embeds 20 lanes of 10.3 Gbps SerDes and four independent embedded 1066 Mbps DDR2/DDR3 controllers.

Demonstrations will take place at the Achronix booth #10, on Wednesday, Dec. 10, (from noon-2:00 p.m. and 5:00–7:00 p.m.) and Thursday, Dec. 11, (from noon-2:00 p.m.). Achronix will also be presenting on the topic, “GHz FPGAs for High Speed Applications,” Wednesday morning in the open session (8:30 a.m. – 9:45 a.m.) titled Communications and Networking Applications.

For more information see [www.fpgasummit.com](http://www.fpgasummit.com)

### About Achronix Semiconductor

Achronix Semiconductor is a privately held fabless corporation based in San Jose, California. Achronix builds the world's fastest field programmable gate arrays (FPGAs) capable of up to 1.5 GHz peak performance. Achronix has sales offices and representatives in the United States, Europe, China, Japan, and Korea, and has research and design offices in Ithaca, N.Y., and Bangalore, India. Find out more at <http://www.achronix.com>.

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