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SEMICONDUCTOR CORPORATION

Press Release | 08/04/2009

# Achronix Demonstrates 1066 Mbps DDR3 Reference Design On New Speedster Plug-in Card Development Board

- ***New Speedster® Plug-in Card (SPC60) development board features DDR3 memory module and provides access to 10.3 Gbps SerDes***
- ***Speedster FPGAs feature four embedded 1066 Mbps DDR3 controllers***

San Jose, Calif. - Achronix Semiconductor, makers of the world's fastest field-programmable gate arrays (FPGAs), today announced availability of a complete 1066 Mbps DDR3 solution for its Speedster FPGAs. The newly developed SPC60, a compact development kit for the Speedster family of 1.5 GHz FPGAs, demonstrates an impressive 1066 Mbps DDR3 reference design available for "out of the box" integration with customer designs; hence, eliminating difficult memory interface implementation and verification challenges.

The SPD60 FPGA has four embedded DDR3 controllers. A full-speed (533 MHz, 1066 Mbps), full-width (72-bit) DDR3 interface can be built using no programmable logic resources. It comes complete with reference design RTL code and documentation, which can be modified as required. Neither the controller nor the reference design requires any license fee.

The development board is designed for systems engineers who need access to both high-end 10.3 Gbps SerDes and DDR3 memory in a compact platform. SPC60 is ideal for applications where there is a growing need for high-performance (both logic and interconnect), such as in the high-end networking, signal processing, and high-performance computing markets.

"The combination of extreme logic throughput and DDR3 memory bandwidth in our Speedster FPGAs enable engineers to create high-performance designs never before achievable," said Yousef Khalilollahi, Achronix vice president of worldwide sales and marketing. "By embedding the 1066 Mbps DDR3 controllers in the FPGA and offering a complete DDR3 reference design, we have removed the burden of FPGA memory interface implementation and verification."

With the SPC60 development board, designers can exercise the full performance of the Speedster device, run prepared designs provided with the kit, configure the device with a new design, and debug that design on a common platform. Achronix-specific and industry-standard development tools are provided, along with a collection of standard reference designs.

The SPC60 board and development tools are available now. (Find a Rep) For more information on the SPC60 development board, please visit www.achronix.com.

**About Achronix Semiconductor**

Achronix Semiconductor is a privately held fabless corporation based in San Jose, Calif. Achronix builds the world's fastest field programmable gate arrays (FPGAs) capable of up to 1.5 GHz peak performance. Achronix has sales offices and representatives in the United States, Europe, China, Japan, and Korea, and has research and design offices in Boston, Mass., Ithaca, N.Y., and Bangalore, India.

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